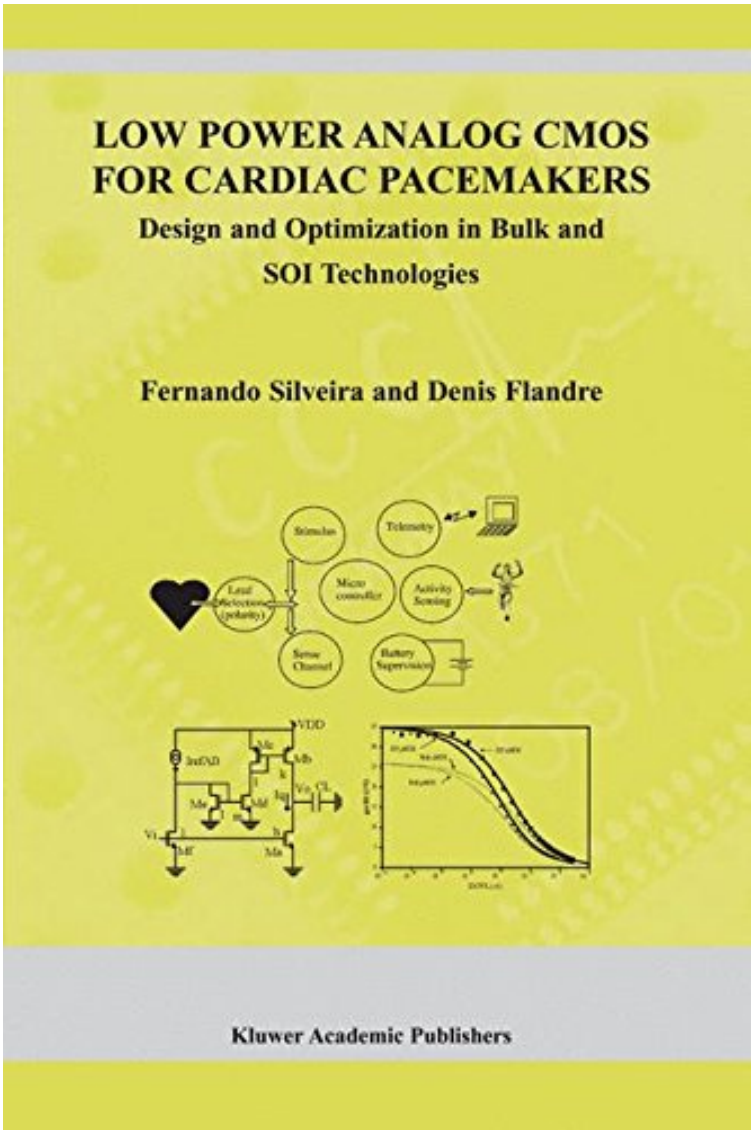
Low Power Analog CMOS for Cardiac Pacemakers: Design and Optimization in Bulk and SOI Technologies (The Springer International Series in Engineering and Computer Science)

Fernando Silveira, Denis Flandre



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Fernando Silveira, Denis Flandre : Low Power Analog CMOS for Cardiac Pacemakers: Design and Optimization in Bulk and SOI Technologies (The Springer International Series in Engineering and Computer Science) before purchasing it in order to gage whether or not it would be worth my time, and all praised Low Power

Analog CMOS for Cardiac Pacemakers: Design and Optimization in Bulk and SOI Technologies (The Springer International Series in Engineering and Computer Science):

Low Power Analog CMOS for Cardiac Pacemakers proposes new techniques for the reduction of power consumption in analog integrated circuits. Our main example is the pacemaker sense channel, which is representative of a broader class of biomedical circuits aimed at qualitatively detecting biological signals. The first and second chapters are a tutorial presentation on implantable medical devices and pacemakers from the circuit designer point of view. This is illustrated by the requirements and solutions applied in our implementation of an industrial IC for pacemakers. There from, the book discusses the means for reduction of power consumption at three levels: base technology, power-oriented analytical synthesis procedures and circuit architecture.